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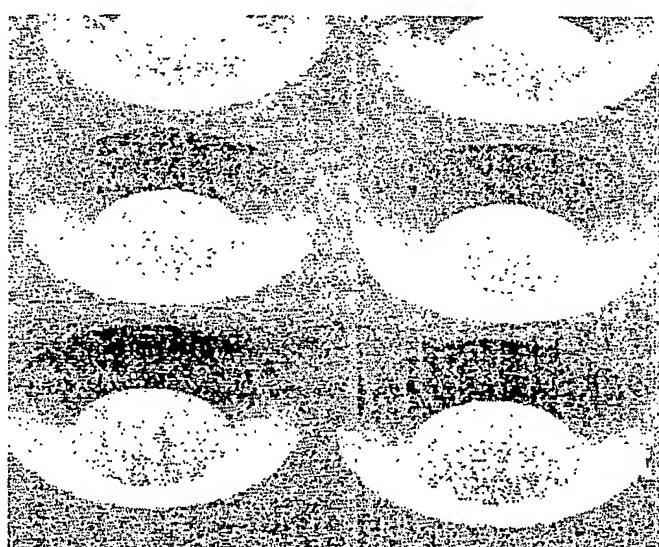
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(54) Title: FLEXIBLE POLYIMIDE CIRCUITS HAVING PREDETERMINED VIA ANGLES



(57) Abstract: A flexible circuit having a polyimide substrate containing at least one chemically etched feature, wherein such etched feature has sidewalls having a predetermined slope angle. It is possible to create etched features having predetermined slopes of from 10 to 70 degrees. Preferably, the flexible circuit was formed from a polyamic acid containing multiple ester groups in its main polymer chain.

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**FLEXIBLE POLYIMIDE CIRCUITS HAVING
PREDETERMINED VIA ANGLES**

Background

The disclosures herein relate generally to flexible circuits and more
5 particularly to flexible circuits having vias with predetermined sidewall angles.

Polyimide film is commonly used as a substrate for flexible circuits. Patterning such substrates primarily is accomplished by such process steps as mechanical punching or drilling, laser ablation and chemical etching. Etched features such as vias or throughholes are commonly present in flexible circuits for
10 reasons of interconnection.

WO 00/11066 discloses polyimide films formed from polyamic acids obtained upon the reaction of p-phenylene bis(trimellitic acid monoester anhydride), hydroxydiphthalic dianhydride, p-phenylenediamine and 4,4'-diaminodiphenyl ether in an organic solvent. The film has a high modulus, 15 high elongation and low coefficients of linear expansion and expansion due to moisture absorption.

The sidewalls of the features show different slopes depending on whether physical or chemical methods are used for forming the feature. For example, punching or drilling basically provides parallel sidewalls. Laser ablation can 20 provide specific slopes, however it is extremely expensive to perform. Chemical etching provides features with angled sidewall slopes. For example, the sidewall slope of a popular commercial polyimide, Kapton™, is from about 20-30 degrees when chemically etched, using a photoresist mask. However, it is extremely difficult to control or alter the angle when chemically etching with a photoresist mask due to a variety of issues, including uncontrolled swelling of the polyimide.

Surface treatments such as corona and plasma etching have also be used to etch polyimides, and to achieve shallow angles. However, surface treatments have not been successful at producing angles in the polyimide above about 15%.

Depending on the applications, significantly different polyimide via slope 30 angles from that normally obtained by chemical etching would be desirable. For example, a shallow angle may be desirable if more stiffness is needed or if the etch

feature must interface with another part. Alternatively, a steep via angle may be desirable if the trace density is high or more flexibility of the circuit is desired.

It has now been discovered that flexible circuits having substrates formed from certain polyimide substrates may be chemically etched by controlled means 5 to form an etched feature having a sidewall of a predetermined slope.

Further, flexible circuits may be formed wherein the substrate has been chemically etched by controlled means such that there are multiple etched features having sidewalls of differing predetermined slopes.

Also, flexible circuits formed from such polyimide substrates do not show 10 the uncontrolled swelling when such an etch is attempted as has been observed with conventional polyimide substrates.

Flexible circuits may also be formed where one etched feature has at least one sidewall having two differing slopes.

Summary of the Invention

15 The instant invention provides a flexible circuit having a polyimide substrate containing at least one chemically etched feature, wherein such etched feature has sidewalls having a predetermined slope angle.

20 The instant invention further provides a process for forming a flexible circuit having a polyimide substrate and containing at least one chemically etched feature, wherein such etched feature has sidewalls with a predetermined slope angle.

More specifically, the instant invention provides a flexible circuit having a polyimide substrate comprising at least one chemically etched feature with sidewalls that have a preselected slope angle of from 15 degrees to 75 degrees.

25 The invention further provides a flexible circuit having a polyimide substrate comprising at least a first and a second chemically etched features having one or more sidewalls of predetermined slopes, wherein said first feature has a predetermined slope which differs from the predetermined slope of the second feature. More features may be included in the plurality which have the same or 30 differing predetermined sidewall slopes as the first and second feature.

Flexible circuits may also be formed where one etched feature has a first sidewall and a second sidewall, each sidewall having a different predetermined slope.

The polyimide substrate of flexible circuit of the invention comprises a 5 polyimide formed from at least one polyamic acid having an ester group therein. One useful monomer is p-phenylene bis(trimellitic acid monoester anhydride) monomer (TMHQ). Other useful monomers include 1,3-diphenol bis(anhydro- trimellitate), 1,4-diphenol bis(anhydro-trimellitate), ethylene glycol bis(anhydro- trimellitate), biphenol bis(anhydro-trimellitate), oxy-diphenol bis(anhydro- trimellitate), bis(4-hydroxyphenyl sulfide) bis(anhydro-trimellitate), bis(4- hydroxybenzophenone) bis(anhydro-trimellitate), bis(4-hydroxyphenyl sulfone) bis(anhydro-trimellitate), bis(hydroxyphenoxybenzene), bis(anhydro-trimellitate), 1,3-diphenol bis(aminobenzoate), 1,4-diphenol bis(aminobenzoate), ethylene glycol bis(aminobenzoate), biphenol bis(aminobenzoate), oxy-diphenol 10 bis(aminobenzoate), bis(4 aminobenzoate) bis(aminobenzoate), and the like. 15

When other types of polyimides are etched, they provide the same slope each time they are etched, and it is not possible to vary the slope, or predetermine a desirable slope. Further, the polyimides conventionally used in electronic packaging are very subject to swelling during etching, which renders control of the 20 etch depth and angle difficult at best. Preferred substrates useful in circuits of the invention do not swell more than fifty percent (50%) by volume when subjected to chemical etchants commonly used to etch flexible circuits.

The invention also provides a process for forming a flexible circuit having a polyimide substrate containing least one chemically etched feature thereon, 25 wherein such etched feature has sidewalls with a predetermined slope angle.

As used herein, these terms have the following meanings.

1. The terms "etched feature" and "chemically etched feature" are synonymous and mean an indentation, via, hole, or the like, which is formed in one or more layers of the flexible circuit by means of etching at least a portion of at 30 least one layer in a chemical bath.
2. The term "sidewalls" refers to the sides of an indentation or hole feature formed by the etching of one or more layers of the substrate. It should be

noted that the number of sidewalls varies with the geometry of the etched feature. For example, a circular etched feature has only one sidewall, which may have one or more slope angles. A square will have four sidewalls, which may each have one or more slope angles.

5 3. The term "slope angle" refers to that angle measured from the bottom of the feature to the top of the etched feature.

All percentages, ratios and amounts herein are by weight unless otherwise specifically noted.

Brief Description of the Figures

10 FIG. 1 is a side representation of an etched feature having at least one sidewall having multiple preselected slope angles.

FIG. 2 is a photograph of an etched feature on a substrate with a corona surface treatment after etching. The sidewall slope angle is 25 degrees.

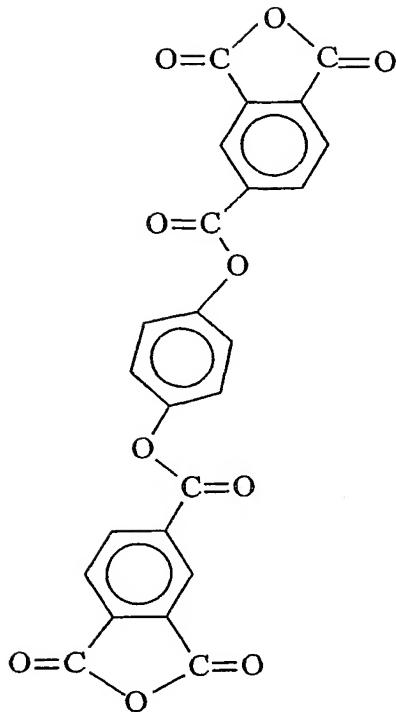
15 FIG. 3 is photograph of an etched feature on a substrate with no surface treatment after etching. The sidewall slope angle is 43 degrees. The etched vias without the surface pre-treatment have sidewalls which are somewhat rough and the via edge is jagged.

20 FIG. 4 is a photograph of a two etched features on a substrate, where one feature has a sidewall slope angle different from the sidewall slope angle of the second feature.

Detailed Description of the Invention

The polyimide film used in the process of the invention is produced from a polyamic acid obtained upon the reaction of a p-phenylene bis(trimellitic acid monoester anhydride), hydroxydiphthalic dianhydride, p-phenylenediamine, and 4,4"-diaminodiphenyl ether in an organic solvent.—The polyimide films are produced from polyamic acid prepared by common synthesis methods.

25 Examples of acid anhydrides which may be used to synthesize polyamic acid to form films useful in circuits and processes of the invention include those having at least one ester chain in the main polymer chain, or "backbone". Useful 30 polyamic acids contain at least 25% ester groups. One example is TMHQ, which has the following structure:



Other monomers include PDA, ODA, pyromellitic dianhydride (PMDA), and the like. These types of monomers are available commercially in Kapton®, Apical®, and the like. The sequence in which the monomers are added for the synthesis is 5 not limiting; however, at least one of the monomers must have an ester group. The total molar amount of the diamine compounds and total molar amount of the acid anhydrides are generally equivalent, having a ratio of from 0.95:1.05 to 1.05:0.95. An exact 1:1 ratio should be avoided as this may cause such a complete polarization that an accompanying excessively high viscosity will result.

10 Organic solvents useful in the polymerization process of the polyimide film include such solvents as N,N-dimethyl formamide, N,N-dimethyl acetamide and similar amides, pyrrolidones such as N-methyl-2-pyrrolidone, and phenols such as p-chlorophenol and cresol. Such solvents may be used alone or may have small amounts of solvents which poorly dissolve polyamic acids such as toluene, 15 tetrahydrofuran, acetone, methylethyl ketone, methanol and ethanol, to adjust to the desired degree of solubility.

 The solution can be formed into a film during imidization. In general, the imidization can be either a thermal imidization or a method using a dehydration agent and/or a catalyst. Useful dehydration agents include aromatic acid

anhydrides and aliphatic acid anhydrides such as acetic anhydride. Examples of catalysts useful in the imidization include pyridine, α -picoline, β -picoline, trimethylamine, dimethylaniline, triethylamine, isoquinoline and similar tertiary amines.

5 The final polyimide film may also include additives such as agents to prevent thermal deterioration, salts, fillers and the like in such amounts as will not substantially change the primary properties. Examples include triphenyl phosphate and benzophenones. The polyimide film is then used to form a flexible circuit.

Another factor in the process of obtaining a feature with a predetermined 10 slope is to vary the concentration of the chemical etchant used, e.g., a concentrated alkali metal hydroxide solution, in the process, the temperature thereof, and the pressure under which the film is etched. Useful etchants are concentrated bases which are water soluble including, but not limited to, potassium hydroxide, sodium hydroxide, and cesium hydroxide. Bases with low water solubility such as lithium 15 hydroxide, aluminum hydroxide, and calcium hydroxide are not useful in processes of the invention as the solution becomes saturated prior to reaching useful concentrations. Concentration of the etchant ranges from 30% to 55%, preferably from 40% to 50%. The etching step of the process of the present invention is accomplished by contacting unmasked areas of the polymeric film with the 20 concentrated base etchant.

The time for etching depends upon the type and thickness of the film to be etched and is typically from about 10 seconds to about 20 minutes. When the preferred etchant, concentrated KOH, is used, the etching time for a 50 micrometer (2 mil) polyimide film is from about 30 seconds to about 180 seconds. The etching 25 solution is generally at a temperature of from about 50°C (122°F) to about 120°C (250°F).

Another variant, as mentioned above, is to use surface treatments, such as plasma treatments and/or corona treatments in order to alter the slope of the angle. The surface treatment also helps to improve the integrity of the via. Interface 30 adhesion between the resist and substrate is an important factor during chemical etching. Any surface contamination, defects, or imperfections such as scratches will influence edge definition of etched features. Surface treatments such as

plasma can clean contamination and repair some of the surface defects by essentially performing a very fine etching on the surface of the substrate. The selection of the type of gas plasma, e.g., oxygen plasma, nitrogen plasma, and the like is important not only for the characteristic of the gas plasma, but because the 5 polymer surface may also chemically interact with some gases, e.g., air or nitrogen, to alter and improve the surface chemical uniformity, and increase the surface wetability during the chemical etching process.

In FIG. 1, for example, one surface, 1 of the flexible circuit was pretreated. This creates a via having a sidewall with a shallow angle slope, 3 while the second 10 surface 2 which received no surface treatment when etched yielded a sidewall having a steeper slope angle 4.

FIG. 2 is a photograph of an etched flexible circuit of the invention having an etched feature where the flexible was subjected to a surface treatment. Smooth side angles were obtained.

15 FIG. 3 is a photograph of an etched flexible circuit which was etched according to the same process conditions as the etched circuit in Figure 2, except that no type of surface treatment was performed before the etch step. As can be seen, the etched vias without the surface pre-treatment have sidewalls which are somewhat rough and the via edge is jagged. The surface treatment used in the etch 20 of Figure 2 has not only changed the angle but improved the smoothness of the etched surface.

25 FIG. 4 is a photograph of two etched features on a substrate, where one feature has a sidewall slope angle different from the sidewall slope angle of the second feature. One feature has sidewall slope angles of about 23°; this feature was etched after surface plasma treatment. The other feature has sidewall slope angles of 49°. This feature was etched without surface pre-treatment. Both features were etched at same condition of 45% KOH concentration and 128°C (200°F) etchant temperature.

30 In the process for producing the flexible circuit with a polyimide substrate, the sequence of procedures may be varied as desired for the particular application.

A typical sequence of steps may be described as follows:

The aqueous processible photoresists are laminated onto both sides of a clean substrate having a polymeric film side and a copper side, using standard laminating techniques. Typically, the substrate consists of a polymeric film layer of from 25 micrometers to 125 micrometers, with the copper layer being from 1 to 5 micrometers thick. The substrate may be made by various methods such as adhesively bonding a polyimide layer onto copper foil, coating liquid polyimide on copper foil or the like.

The thickness of the photoresist is from 35 to 50 micrometers. The photoresist is then exposed on both sides to ultraviolet light or the like, through a mask, crosslinking the exposed portions of the resist. The resist is then developed with a dilute aqueous solution, e.g., a 0.5-1.5% carbonate solution, until desired patterns are obtained on both sides of the laminate. The copper side of the laminate is then further plated to desired thickness. The laminate is then placed into a bath of concentrated base at a temperature of from about 50°C to about 120°C which etches the portions of the polymeric film not covered by the crosslinked resist. This exposes certain areas of the original thin copper layer. The resist is then stripped off both sides of the laminate in a 2-5% solution of an alkaline metal hydroxide at from about 20°C to about 80°C, preferably from about 20°C to about 60°C. Subsequently, the original thin copper layer is etched where exposed with an etchant which does not harm the polymeric film, e.g., Perma-etch™, available from Electrochemicals, Inc.

The surface treatment, e.g., plasma or corona treatments, when desirable, can be performed at either of two different times. It may be done after initial cleaning of the substrate, prior to the resist being laminated onto the substrate. It may also be after the photoresist has been laminated and etched prior to the stripping of the resist.

In an alternate process, the aqueous processible photoresists are laminated onto both sides of a substrate having a polymeric film side and a copper side, using standard laminating techniques. The substrate consists of a polymeric film layer 30 12 micrometers to 125 micrometers thick with the copper layer being from 12 to 40 micrometers thick. The photoresist is then exposed on both sides to ultraviolet light or the like, through a suitable mask, crosslinking the exposed portions of the

resist. The image is then developed with a dilute aqueous solution until desired patterns are obtained on both sides of the laminate. The copper layer is then etched to obtain circuitry, and portions of the polymeric layer thus exposed. An additional layer of aqueous photoresist is then laminated over the first resist on the copper 5 side and crosslinked by flood exposure to a radiation source in order to protect exposed polymeric film surface (on the copper side) from further etching. Areas of the polymeric film (on the film side) not covered by the crosslinked resist are then etched with the concentrated base at a temperature of from about 70°C to about 120°C, and the photoresists are then stripped from both sides with a dilute basic 10 solution.

Again, in this process the surface treatment, e.g., plasma or corona treatment can be performed at either of two different times, after initial cleaning of the substrate, prior to the resist being laminated onto the substrate, or after the photoresist has been laminated and etched prior to the stripping of the resist.

15 Other steps may also be included, such as soaking the film in hot water before or after the etching bath. Acid baths may also be used as a post-etching neutralization.

20 To create finished products such as flexible circuits, interconnect bonding tape for "TAB" (tape automated bonding) processes, microflex circuits, and the like, further layers may be added and processed, the copper plating may be plated with gold, tin, or nickel for subsequent soldering procedures and the like according to conventional means.

The following examples are meant to be illustrative and are not intended to limit the scope of the invention which is expressed solely by the claims.

25

Examples

Examples 1C and 2

30 A 2 mil polyimide film of Kapton® E, (Ex. 1C) and an identical sample of Apical® HP (Ex. 2) were prepared under the same conditions. A photoresist was laminated onto the samples followed by standard exposing and developing processes. Both samples were etched at 45% KOH and 195°F. Samples were not rinsed after etching and were examined by cross sectioning to reveal the degree of swelling of the polyimide substrate. Example 1C film showed extensive swelling

and pushing up of the resist, which sometimes led to a separation between the polyimide film and resist. Such swelling prevents the etch angle from being easily controlled because the pushing up of the photoresist causes undercut and loss of pattern definition (feature size, sidewall slope angle). Also, swelling of the 5 polyimide can cause a localized delamination of the resist, which can lead to irregular shape due to etchant migration into the delaminated area.

Example 2 film shown very little, if any, swelling. The polyimide dissolved off during etching in comparison with swelling of the comparative polyimide film 1C. There is no need for hot water rinsing after chemical etching 10 and the etched through-hole sidewall slope becomes controllable.

Examples 3-4

A polyimide substrate available as Apical® HP was treated by a combination of air corona at 200 W-min/m^2 and oxygen plasma at 800nA and 15 65mTorr . The film was then chemically etched by 45% potassium hydroxide (KOH) at 200°F . The through-hole features had sidewall slope of 13 degrees.

A second identical polyimide substrate was chemically etched without treatment and a sidewall slope angle of 63 degrees was obtained.

Additional samples showed 20-35 degree etched sidewall angle were obtained when air corona treatment was used alone at the power range of 50-200 20 W-min/m^2 . Etched angles became even shallower with oxygen plasma than air corona. Plasma treatment level and time also showed some effects on the etched angles. Longer time and higher power of treatment normally make etched sidewall angle even shallower at same etching conditions.

Example 5

Altering Via Angle by Adjusting the Etchant Concentration

A polyimide substrate, Apical® HP, was chemically etched using different strength KOH solutions ranging from 40-50% at 200°F for 60-90 seconds depending on the artwork design. See the following table for resulting etch angle. 30 Etch angles are similar regardless of the artwork design, but depend only on the difference in the etchant concentration.

KOH Concentration	Resulting Via Angle	
	Apical ® HP	Kapton E
40%	$20^\circ \pm 5^\circ$	$25^\circ \pm 5^\circ$
45%	$45^\circ \pm 5^\circ$	$25^\circ \pm 5^\circ$
50%	$63^\circ \pm 5^\circ$	$25^\circ \pm 5^\circ$

A similar experiment was repeated using Kapton® E film which does not have the same ester functionality along the backbone. The etch concentrations and etch conditions were otherwise the same as those used for the Apical® HP film.

5 The variation of the etch angle due to variation of the KOH concentration was insignificant.

Example 6

Variation in Etch Angle in Samples with an Air Corona Pretreatment

A polyimide substrate, Apical® HP was pretreated by exposure to an air 10 corona at 50, 100, 150, and 200 W-min/m². The film was then etched with a 45% KOH solution at 200°F for 60-90 seconds depending on the artwork design. The resulting via's sidewall slope was $20^\circ \pm 3^\circ$, which is very similar to the angle obtained using the 40 weight percent KOH solution except that there was a significant improvement in the edge definition and a less variation in the actual 15 side wall angle. However, the angle did not vary based on the level of air corona pretreatment applied.

Example 7

Variation in Etch Angle with Air Corona Pretreatment and KOH Concentration

A polyimide substrate, Apical® HP was pretreated by exposure to an air 20 corona at 200 W-min/m². The film was then etched with a 50% KOH solution. The resulting via's side wall slope was $30^\circ \pm 3^\circ$. The etched via sidewalls have much better edge definition and less variation in the actual sidewall angle from one sample to another than if KOH etching was done without the pretreatment step.

KOH Concentration	Pretreatment	Etch Angle	From Example
50 w/w%	Air Corona	$30^\circ \pm 3^\circ$	7
45 w/w%	Air Corona	$20^\circ \pm 3^\circ$	6
50 w/w%	No	$63^\circ \pm 5^\circ$	5

What is Claimed is:

1. A flexible circuit having a polyimide substrate containing at least one chemically etched feature, wherein such etched feature has sidewalls having a predetermined slope angle, wherein said polyimide swells less than 50 percent by 5 volume when subjected to chemical etchants.
2. A flexible circuit according to claim 1 wherein said polyamic acid contains at least 25% percent ester groups.
3. A flexible circuit according to claim 2 wherein said polyamic acid is p-phenylene bis(trimellitic acid monoester anhydride) monomer (TMHQ).
- 10 4. A flexible circuit according to claim 3 wherein said polyimide substrate was formed a polyamic acid obtained upon the reaction of a p-phenylene bis(trimellitic acid monoester anhydride), hydroxydiphthalic dianhydride, p-phenylenediamine, and 4,4"-diaminodiphenyl ether in an organic solvent.
- 15 5. A flexible circuit according to claim 1 wherein said slope angle is between 10 degrees and 75 degrees.
6. A flexible circuit according to claim 5 wherein said angle is between 10 degrees and 60 degrees.
- 20 7. A flexible circuit according to claim 1 having a polyimide substrate comprising at least a first and a second chemically etched features having sidewalls of predetermined slopes, wherein said predetermine slope of said sidewalls of said first feature differ from the predetermined slope of said sidewalls of said second feature.
- 25 8. A flexible circuit according to claim 1 wherein at least one of said chemically etched feature has at least one sidewall, each sidewall having at least two different predetermined slopes.

9. A flexible circuit according to any of claims 1, 5, 7 or 8 wherein said surface treatment is selected from the group consisting of gas plasma treatment and corona treatment.

10. A flexible circuit according to claim 1 having multiple chemically 5 etched features.

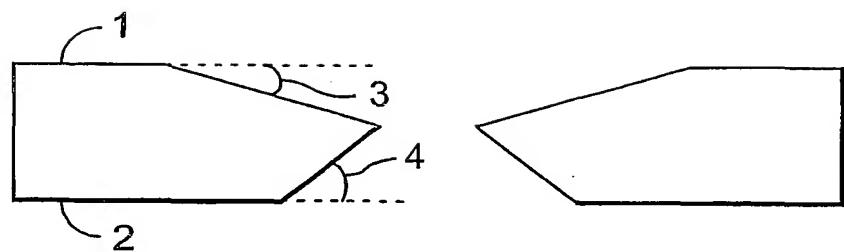
11. A flexible circuit according to claim 1 wherein said chemically etched feature is selected from a throughhole and a blind via.

12. A process for forming a flexible circuit according to claim 1, wherein such etched feature has sidewalls with a predetermined slope angle 10 comprising the step of placing the circuit in an etchant solution selected from the group consisting of potassium hydroxide, sodium hydroxide, and cesium hydroxide.

13. A process for forming a flexible circuit according to claim 12 wherein said etchant solution has a concentration of from 30% to 50% by weight.

15 14. A process for forming a flexible circuit according to claim 13 further comprising a surface treatment step selected from the group consisting of corona treatments and plasma treatments.

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**FIG. 1****FIG. 2****FIG. 3**

2/2

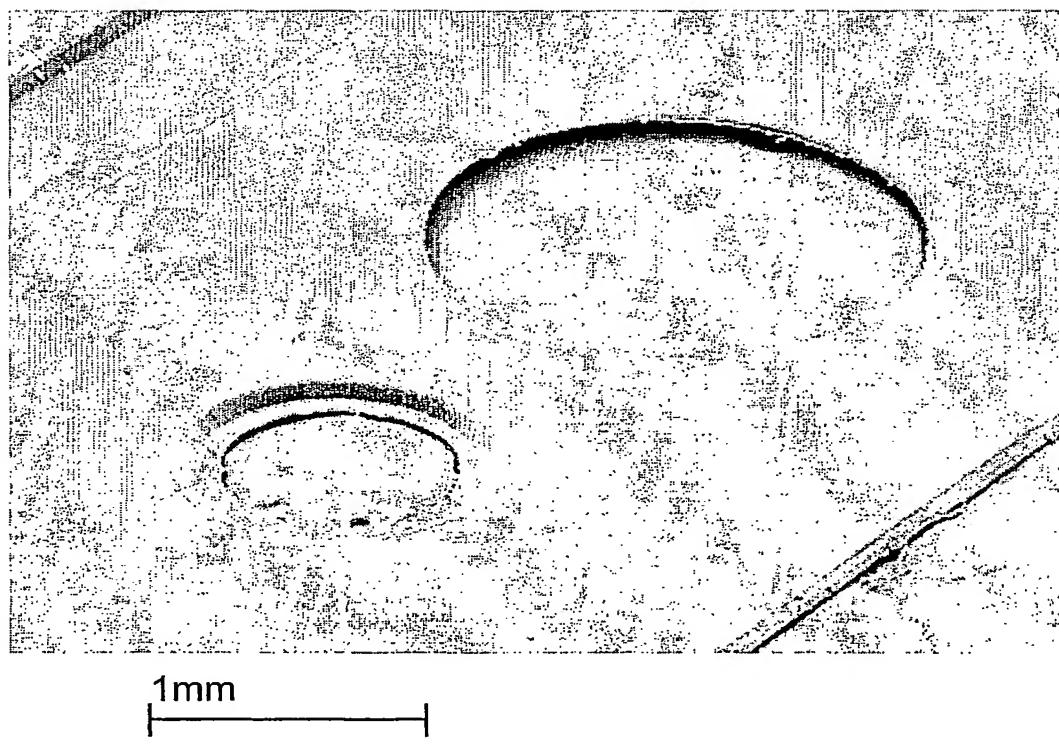
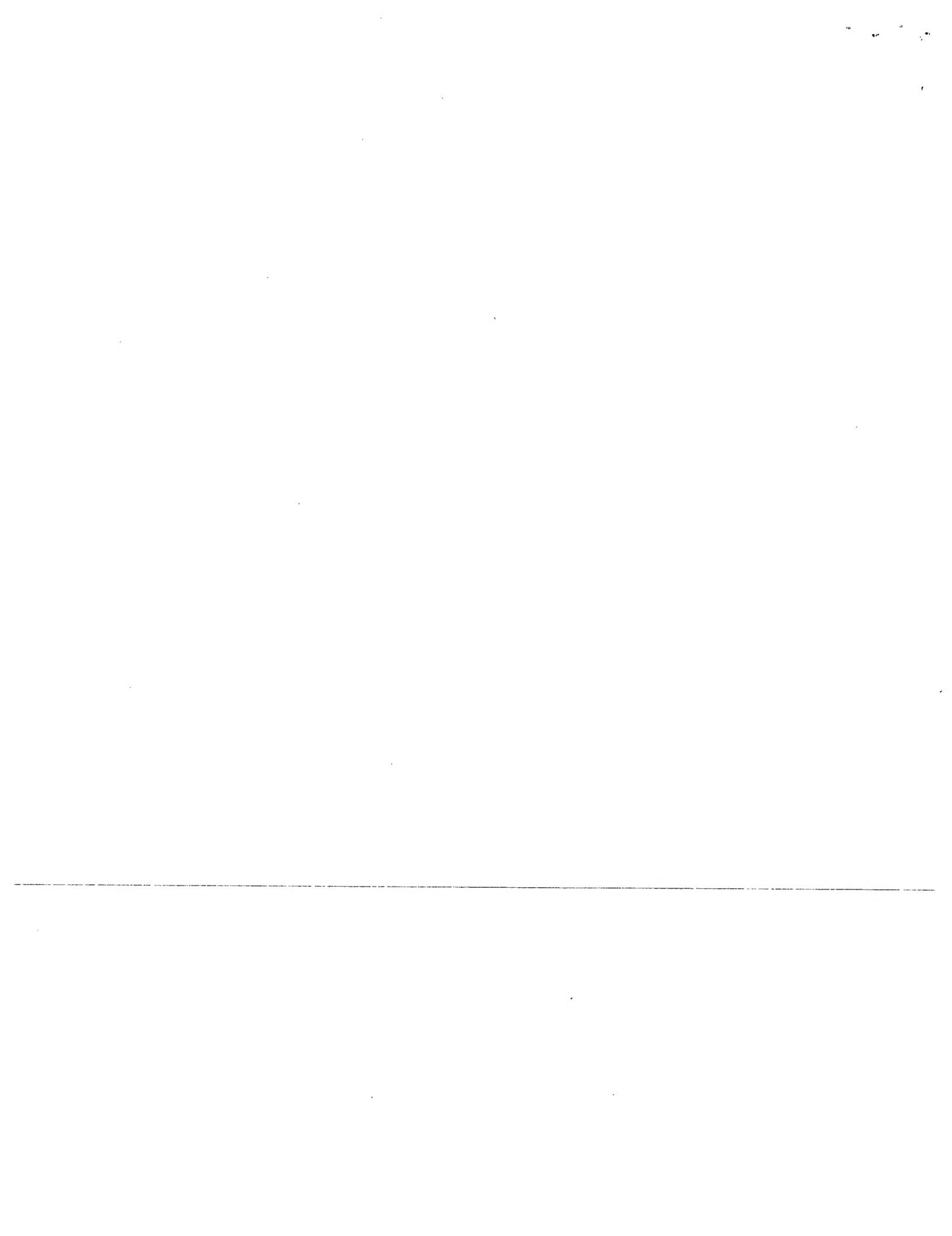


FIG. 4



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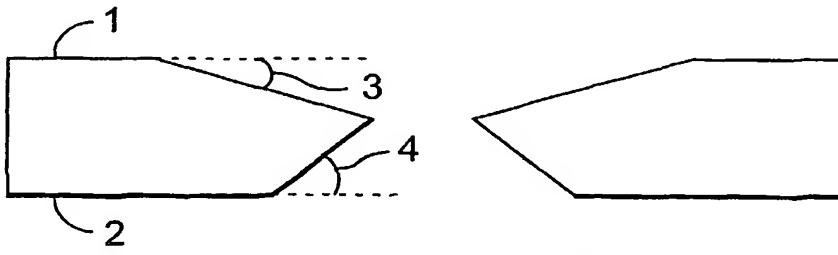
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(54) Title: FLEXIBLE POLYIMIDE CIRCUITSUBSTRATES HAVING PREDETERMINED SLOPEDVIAS



polyamic acid containing multiple ester groups in its main polymer chain.

(57) Abstract: A flexible circuit having a polyimide substrate containing at least one chemically etched feature, wherein such etched feature has sidewalls having a predetermined slope angle. It is possible to create etched features having predetermined slopes of from 10 to 70 degrees. Preferably, the flexible circuit was formed from a

INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER
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According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 7 H05K

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	DE 23 38 647 A (SIEMENS AG) 20 February 1975 (1975-02-20) the whole document ---	1-14
A	US 5 378 314 A (SCHMIDT WALTER ET AL) 3 January 1995 (1995-01-03) the whole document ---	1-14
A	DE 44 05 228 C (SCHOELLER & CO ELEKTRONIK ;DEGUSSA (DE)) 6 July 1995 (1995-07-06) the whole document ---	1-14

Further documents are listed in the continuation of box C.

Patent family members are listed in annex.

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Date of the actual completion of the international search

22 July 2002

Date of mailing of the international search report

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT		
Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	<p>"COMBINED LASER PROCESSING OF VIAS AND CIRCUITRY IN FLEX-CIRCUITS" IBM TECHNICAL DISCLOSURE BULLETIN, IBM CORP. NEW YORK, US, vol. 38, no. 5, 1 May 1995 (1995-05-01), pages 607-608, XP000519699 ISSN: 0018-8689 the whole document</p> <p>-----</p>	1-14

INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

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